

| Type | L # | Hits | Search Text | DBs | Time Stamp | Comments | Error Definition | Errors |
|-------|-----|------|--|---|---------------------|----------|------------------|--------|
| 1 BRS | L1 | 359 | ((flexible or bendable) near tape) same rigid\$ | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:38 | | | 0 |
| 2 BRS | L2 | 6 | assembly and tape and carrier and semiconductor | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:41 | | | 0 |
| 3 BRS | L3 | 5 | apparatus and tape and carrier and semiconductor | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:42 | | | 0 |
| 4 BRS | L4 | 3 | (index adj hole) near tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:44 | | | 0 |
| 5 BRS | L5 | 8 | (index adj hole) with tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:47 | | | 0 |
| 6 BRS | L6 | 20 | ((index adj hole) same (tape or film)) and (carrier or support) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:52 | | | 0 |
| 7 BRS | L7 | 0 | (solder adj resist) adj carrier | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:54 | | | 0 |
| 8 BRS | L8 | 3 | (solder adj resist) adj (frame or bar) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:53 | | | 0 |

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|----|------|-----|------|---|---|---------------------|----------|------------------|--------|
| 9 | BRS | L9 | 106 | (solder adj resist) with carrier | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:55 | | | 0 |
| 10 | BRS | L10 | 4 | (solder adj resist) near carrier | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 12:58 | | | 0 |
| 11 | BRS | L11 | 11 | (index adj hole) with (tape or (thin adj film)) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:00 | | | 0 |
| 12 | BRS | L12 | 447 | bt adj resin | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:15 | | | 0 |
| 13 | BRS | L13 | 0 | L12 near carrier | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:03 | | | 0 |
| 14 | BRS | L14 | 14 | (bt adj resin) with (carrier or frame or bar or support or reinforcement) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:15 | | | 0 |
| 15 | BRS | L15 | 9 | L14 and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:04 | | | 0 |
| 16 | BRS | L17 | 260 | L12 and semiconductor | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 13:16 | | | 0 |

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|--------|-----|------|-------------------------------------|---|---------------------|----------|------------------|--------|
| 17 BRS | L16 | 137 | L12 and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 13:55 | | | 0 |
| 18 BRS | L18 | 199 | 438/111.ccls. | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 13:55 | | | 0 |
| 19 BRS | L19 | 89 | 438/111.ccls. and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 13:57 | | | 0 |
| 20 BRS | L20 | 17 | 438/459.ccls. and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 13:58 | | | 0 |
| 21 BRS | L21 | 81 | 438/113.ccls. and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 13:59 | | | 0 |
| 22 BRS | L22 | 95 | 438/108.ccls. and tape | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 14:00 | | | 0 |
| 23 BRS | L23 | 159 | 361/778.ccls. | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 14:01 | | | 0 |
| 24 BRS | L24 | 35 | 361/778.ccls. and (tape or carrier) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM ; TDB | 2001/08/09 14:06 | | | 0 |

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments | Error Definition | Errors |
|----|------|-----|------|-------------------------------------|---|---------------------|----------|------------------|--------|
| 25 | BRS | L25 | 171 | 361/807.ccls. and (tape or carrier) | USPAT; US-PGPU B; EPO; JPO; DERWENT ; IBM TDB | 2001/08/09 14:06 | | | 0 |